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L7: Entry 1 of 2

File: JPAB

Dec 10, 1999

PUB-NO: JP411338714A
DOCUMENT-IDENTIFIER: JP 11338714 A
TITLE: HIGH-SPEED ADDRESS SWITCHING DEVICE

PUBN-DATE: December 10, 1999

INVENTOR-INFORMATION:

NAME

COUNTRY

KODA, YOSHIHIRO

INT-CL (IPC): G06 F 9/46; G06 F 9/46

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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L7: Entry 2 of 2

File: DWPI

Dec 10, 1999

DERWENT-ACC-NO: 2000-211201
DERWENT-WEEK: 200019
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TITLE: Address switching device of computer system, has management tables in memory to show setting situation of execution environment of tasks and other processes

PRIORITY-DATA: 1998JP-0139963 (May 21, 1998)

PATENT-FAMILY:

PUB-NO	PUB-DATE	LANGUAGE	PAGES	MAIN-IPC
JP 11338714 A	December 10, 1999		016	G06F009/46

INT-CL (IPC): G06 F 9/46

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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